
APPLICATION DATA SHEET FORM

Application Information

Inventor Information

Inventor One Given Name:: Cesare
Family Name:: CLEMENTI
Postal Address Line One:: Via Castelmorrone 4
City:: Busto Arsizio
State or Province:: ITALY
Postal or Zip Code::
Citizenship Country:: Italy

Inventor Two Given Name:: Gabriella
Family Name:: GHIDINI
Postal Address Line One:: Via Galla Placidia 10
City:: Milano
State or Province:: ITALY
Postal or Zip Code::
Citizenship Country:: Italy

Inventor Three Given Name:: Carlo
Family Name:: RIVA
Postal Address Line One:: Via L. Manara 8
City:: Renate Brianza
State or Province:: ITALY
Postal or Zip Code::
Citizenship Country:: Italy

Correspondence Information

Name Line One:: James H. Morris
Address Line One:: Wolf, Greenfield & Sacks, P.C.
Address Line Two:: 600 Atlantic Avenue
City:: Boston
State or Province:: MA
Country:: USA
Postal or Zip Code:: 02210
Telephone One:: 617-720-3500
Fax Number:: 617-720-2441
Electronic Mail:: JMorris@wolfgreenfield.com

Application Information

Title Line One:: Process For Forming An Integrated Circuit Comprising
Non-Volatile Memory Cells And Side Transistors Of
At Least Two Different Types, And Corresponding IC
Total Drawing Sheets:: 2
Application Type:: Non-Provisional
Docket Number:: S1022/8827

Representative Information

Representative Customer Number:: 23628

Continuity Information

This application is a:: Continuation
>Application One:: 09/123,295
Filing Date:: July 28, 1998

which is a:: Divisional
>Application Two:: 08/670,179
Filing Date:: June 20, 1996

Prior Foreign Applications

Foreign Application One:: 95830282.0
Filing Date:: June 30, 1995
Country:: Europe
Priority Claimed:: Yes

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